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Silicon N Channel MOS FET High Speed Power Switching

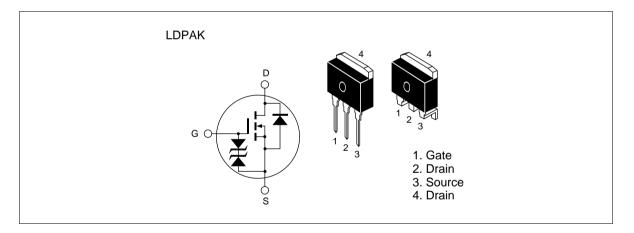


ADE-208-567D (Z) 5th. Edition Jul. 1998

Features

- Low on-resistance
 - $R_{DS(on)} = 7m\Omega$ typ.
- 4V gate drive devices.
- High speed switching

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	30	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	50	А	
Drain peak current	Note1 D(pulse)	200	A	
Body-drain diode reverse drain current	I _{DR}	50	А	
Channel dissipation	Pch Note2	75	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1. $PW \le 10\mu s$, duty cycle $\le 1 \%$

2. Value at $Tc = 25^{\circ}C$

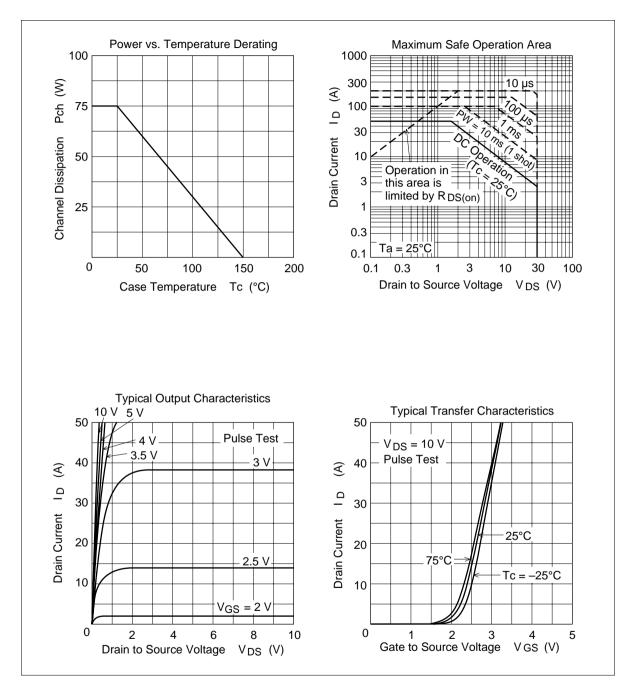
Electrical Characteristics (Ta = 25°C)

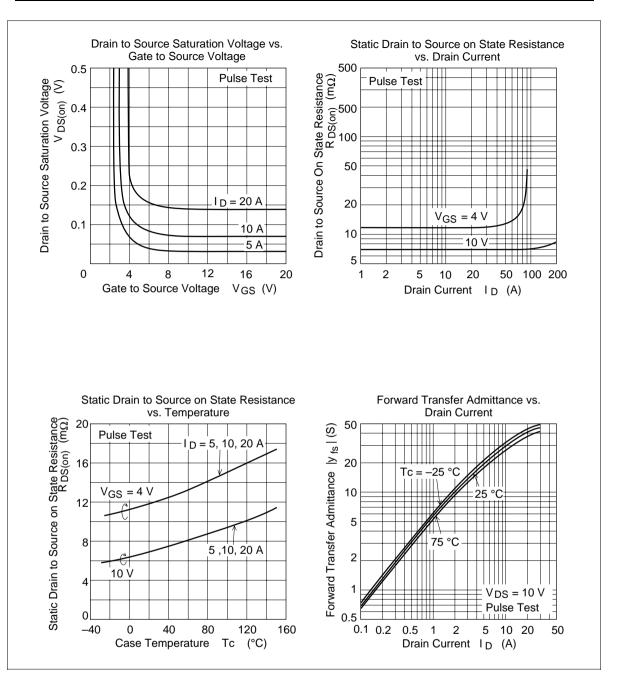
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	_	_	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	10	μΑ	$V_{\rm DS} = 30 \ V, \ V_{\rm GS} = 0$
Gate to source leak current	I _{GSS}	—	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{\rm D} = 1$ mA, $V_{\rm DS} = 10$ V
Static drain to source on state resistance	$R_{DS(on)}$	—	7.0	10	mΩ	$I_{\rm D} = 25$ A, $V_{\rm GS} = 10$ V ^{Note3}
Static drain to source on state resistance	$R_{DS(on)}$	—	12	18	mΩ	$I_D = 25A, V_{GS} = 4V^{Note3}$
Forward transfer admittance	y _{fs}	25	45	_	S	$I_{\rm D} = 25$ A, $V_{\rm DS} = 10$ V ^{Note3}
Input capacitance	Ciss	_	2000	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	1500	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	350	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	—	20		ns	$V_{gs} = 10V, I_{D} = 25A$
Rise time	t,	_	330		ns	$R_{L} = 0.4\Omega$
Turn-off delay time	t _{d(off)}	_	190		ns	
Fall time	t _f	_	190		ns	
Body-drain diode forward voltage	V_{DF}	_	0.95		V	$I_{F} = 50A, V_{GS} = 0$
Body–drain diode reverse recovery time	t _{rr}	—	60		ns	I _F = 50A, V _{GS} = 0 diF/ dt =50A/μs

Note: 3. Pulse test

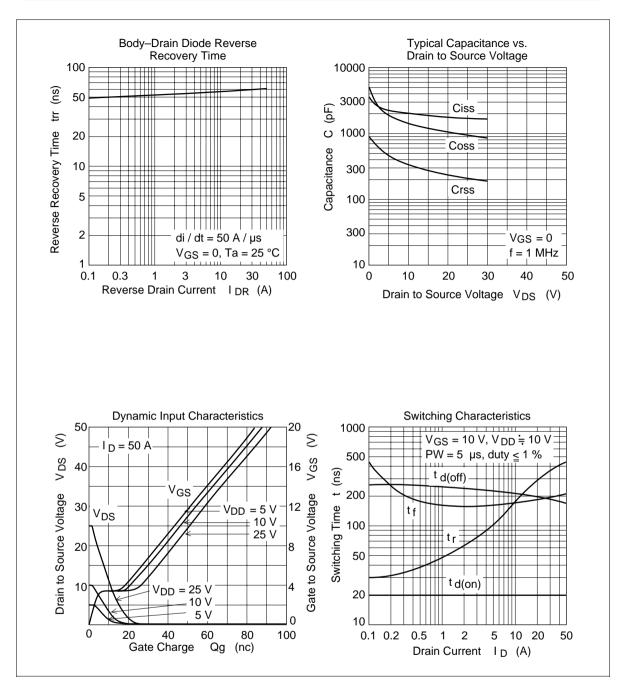
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Main Characteristics

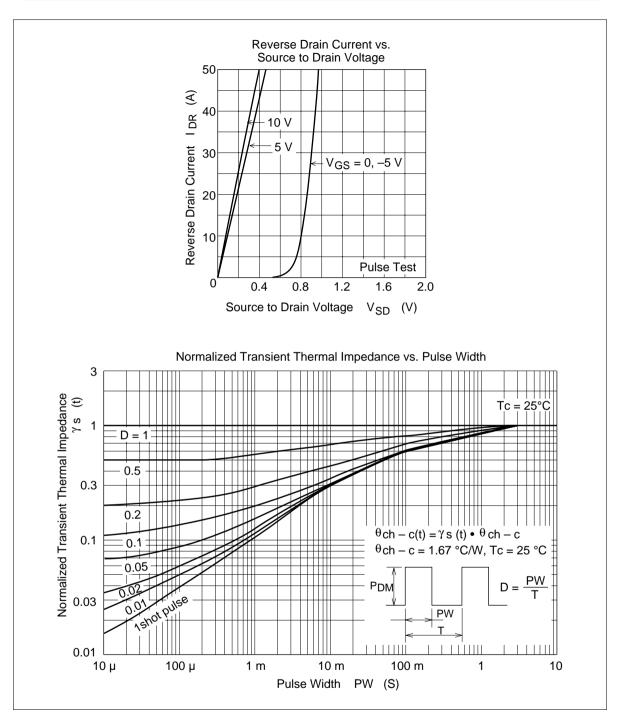


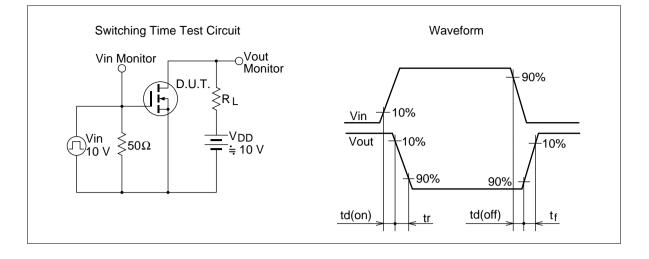


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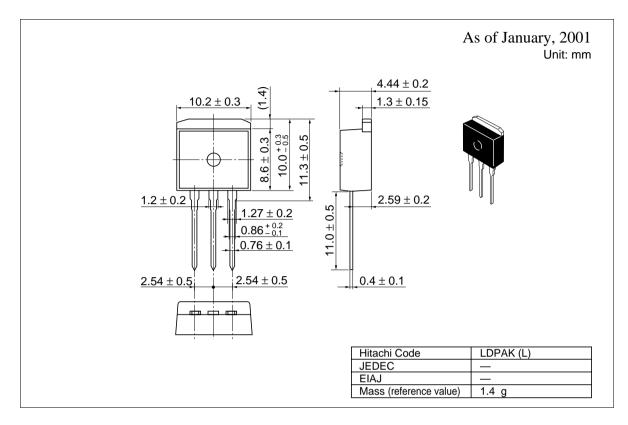


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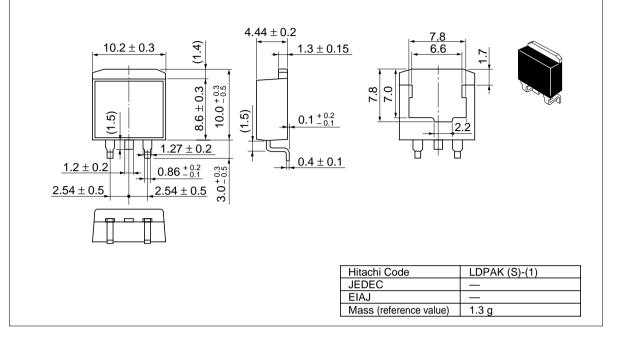


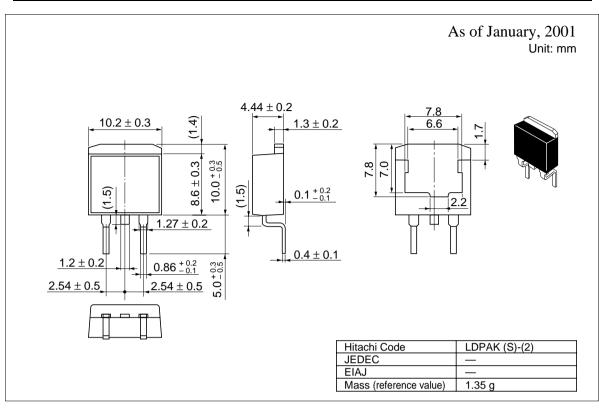


Package Dimensions



As of January, 2001 Unit: mm





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